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shorting connector memory module

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Patents

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[Sort by relevance](#) | [Sort by date \(new first\)](#) | [Sort by date \(old first\)](#)[Memory device supporting a dynamically configurable core organization](#)

US Pat. 6886364 - Filed Nov 22, 2002 - Rambus Inc.

A shorting connector with a memory module inserted is functionally identical to the connector 610 in FIG. 7. As noted above, the general signal line scheme ...

[Connecting apparatus for electrically connecting memory modules to a printed ...](#)

US Pat. 4860892 - Filed Feb 3, 1989 - Wang Laboratories, Inc.

Two channel mount assemblies make up the memory module connector 300 in the ...
The data lead connectors or shorting bars 502 connect corresponding pins 504 ...

[Modular computer memory circuit board](#)

US Pat. 5126910 - Filed May 10, 1990 - Compaq Computer Corporation

SB, when a memory module 400 is installed, connectors 306 and 404 are mated ...
board 300 were fully connector 404 ground line B3', thereby shorting power ...

[High speed stackable memory system and device](#)

US Pat. 8109929 - Filed Jul 29, 1998 - Agilent Technologies, Inc.

3A and 3B depict typical memory modules 5, according to the prior art. Each memory module typically has nine memory chips 4 on a side and a row of connector ...

[Semiconductor memory module and module system](#)

US Pat. 6388886 - Filed Nov 20, 2000 - Mitsubishi Denki Kabushiki Kaisha

Thus, a large capacity module system can be implemented readily. According to the sixth aspect of the present invention, the second connector is fixedly ...

[Processor-inclusive memory module](#)

US Pat. 5710733 - Filed Jan 22, 1996 - Silicon Graphics, Inc.

lines or cache buses, a memory module which is capable of US Pat. ... is attached to mother board 14 via edge connector has an address line formed therein. ...

[Radial arm memory bus for a high availability computer system](#)

US Pat. 6530033 - Filed Oct 28, 1999 - Hewlett-Packard Company

2 and 3A-B, a short of two connector pins together will short out entire bus. However, in the memory configuration of FIGS. 4A-C, although the memory module ...

[Memory system](#)

US Pat. 6519173 - Filed Jun 5, 2002 - Hitachi, Ltd.

Connector terminal sequences or 15 rows 130 are formed on upper and lower ...
9 is a perspective view showing the mounting of the memory module 1 in ...

[Memory module including module data wirings available as a memory access ...](#)

US Pat. 6828538 - Filed Mar 26, 2002 - Hitachi, Ltd.

26A, 26B and 26C respectively show a dummy 5 memory module 2A available together with the second Similarly, two single connector terminal portions ...

[Processor-inclusive memory module](#)

US Pat. 5959437 - Filed Jan 27, 1997 - Silicon Graphics, Inc.

10 15 n 25 PROCESSOR-INCLUSIVE MEMORY MODULE This application is a divisional ...
The cache bus extends from edge connector 24 to memory component 16. ...

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